



The diagram consists of two vertically aligned plots sharing a common horizontal time axis labeled t .

The top plot shows the gate voltage U_{Gate} on the vertical axis. It is a periodic square wave with a constant high level labeled U_{max} and a constant low level. The period of the signal is indicated by a horizontal bracket labeled T_p .

The bottom plot shows the load current I_L on the vertical axis. It is a periodic square wave with a constant high level labeled I_{max} and a constant low level. The transitions of I_L occur at the same time as the transitions of U_{Gate} , indicating that the current is in phase with the gate voltage.

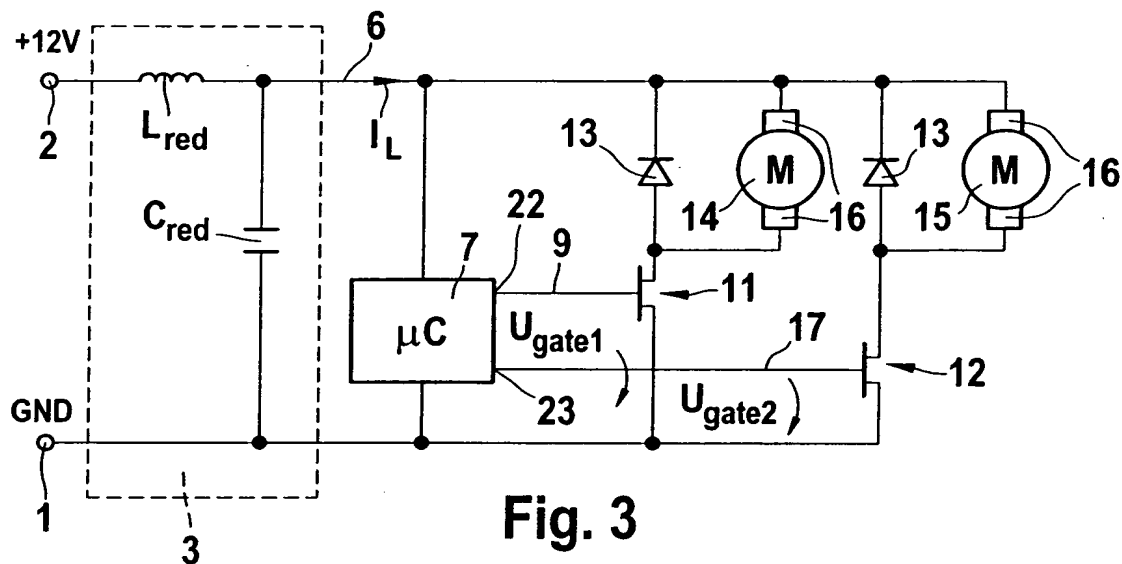


Fig. 4

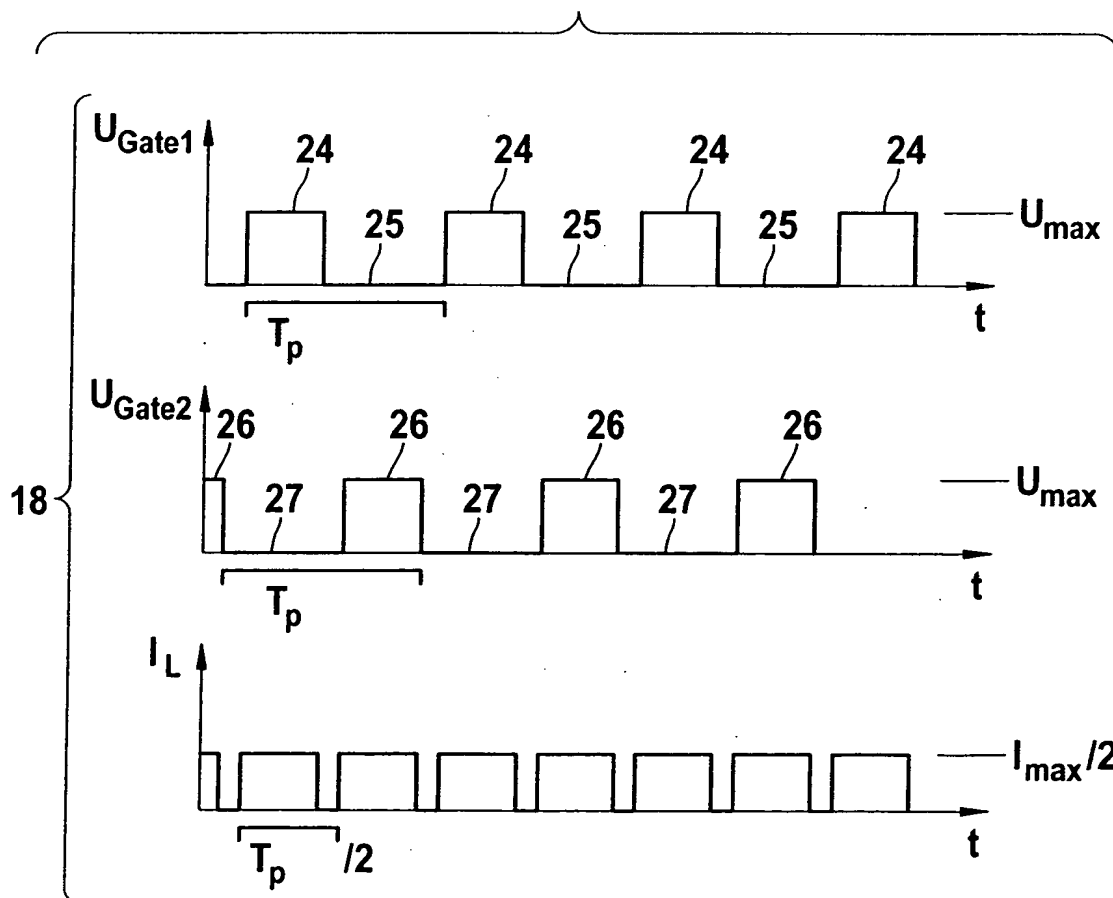


Fig. 5

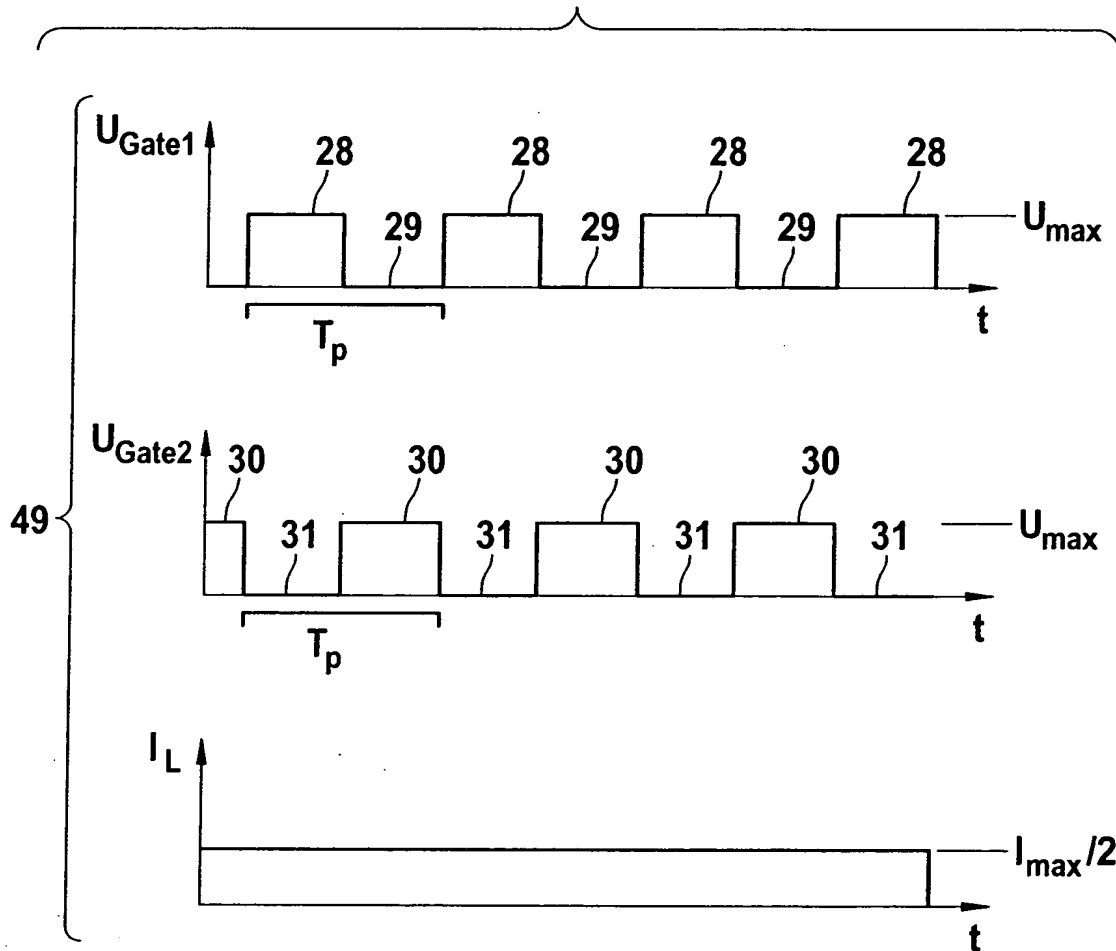


Fig. 6

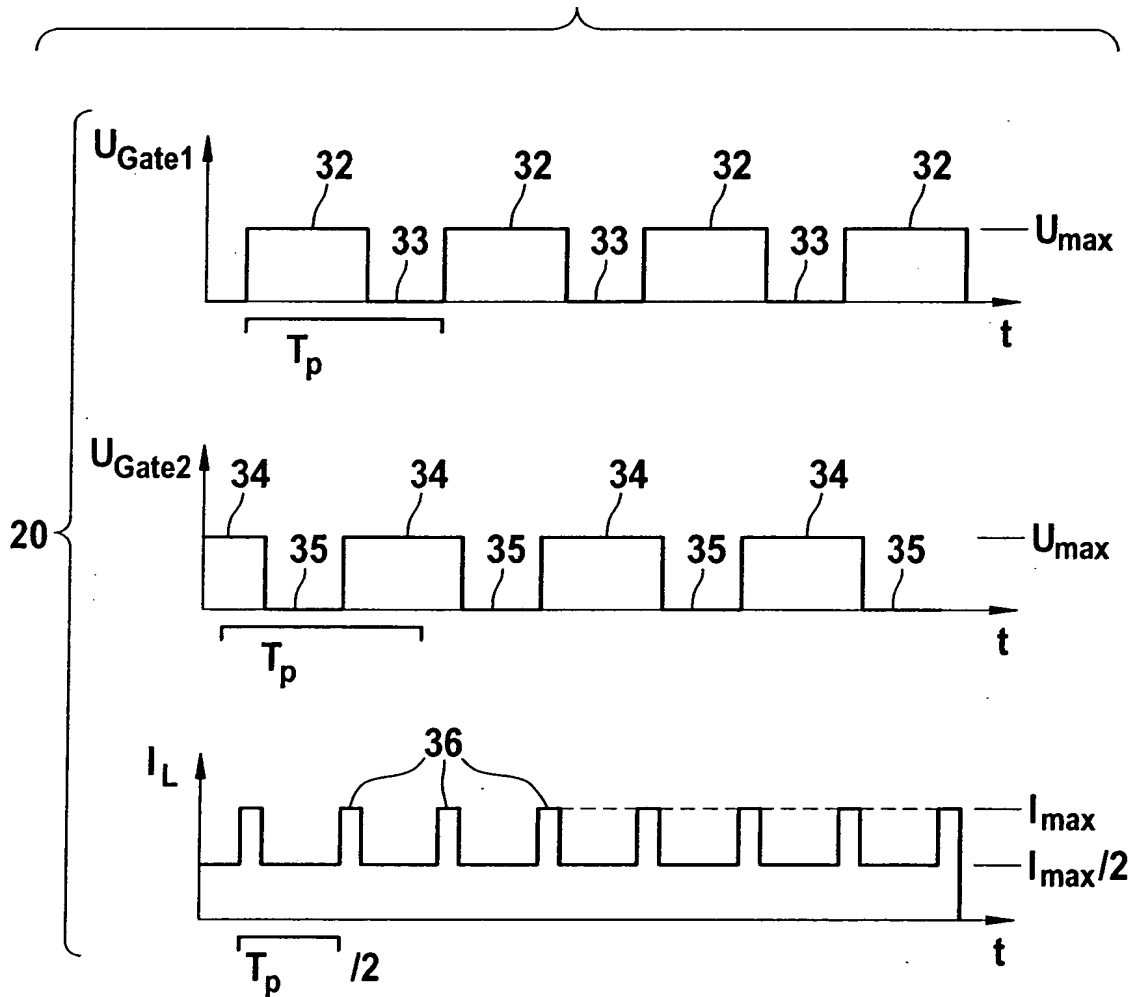


Fig. 7

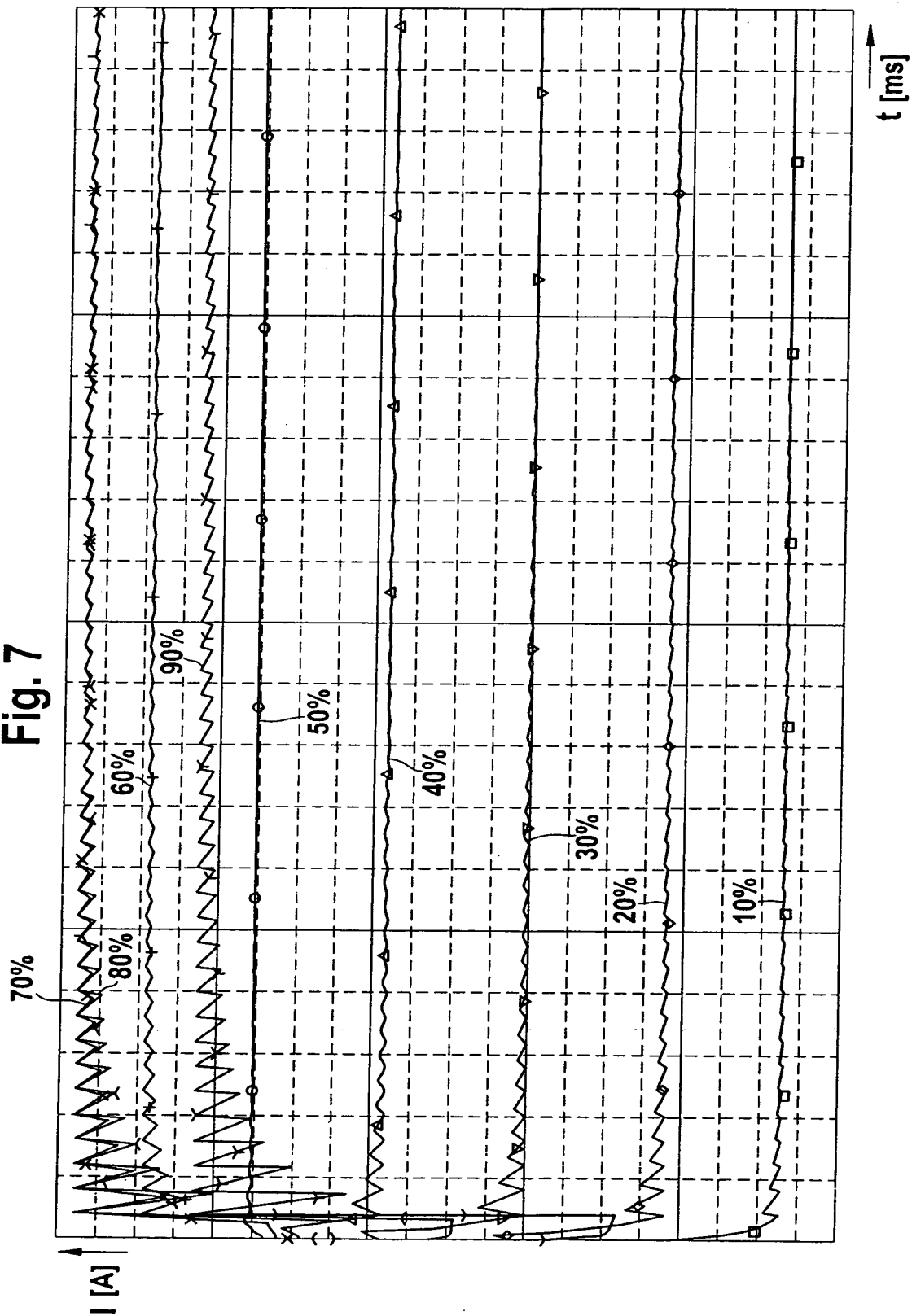


Fig. 8

